

1 2 3 4 5 6 7 8

NOTES:

1. Material:

Insulator: LCP, UL 94V-0 Rated.

Color: Black.

CAP: LCP, UL 94V-0 Rated.

Color: Black.

Contact: Phosphor Bronze.

Plating: Gold Plating over Nickel 50u" Min. on Contact Area;
Tin 80u" Min. Plating over Nickel 50u" Min. on Solder Tail.

Shell: Stainless Steel.

Plating: Tin 80u" Min. Plating over Nickel 50u" Min.

2. Mechanical:

Insertion Force: 35N Max.

Withdrawal Force: 15N Min.

Durability: 50Cycles.

3. Electrical Specification:

Current Rating: 0.5A per Contact.

LLCR: 50mΩ per Pin Max.

LLCR 10mΩ Max Change of all Pins.

Insulation Resistance: 100MΩ Min.

Dielectric Withstanding Voltage: 300V AC R.M.S.

4. Environmental:

Operating Temperature: -55°C to +85°C.

Soldering Temperature: 265°C±5°C for 10Seconds Max.

If EMI Shell Solder Lead is Connected to The Adjacat GND Pin
Viatrace, Need to Be Isolated by Solder Resist.

Part Number:

CU3E-R20VSG0-A1

USB3.2 E Type Series

R: Receptacle

20: 20Pins

V: Vertical Type

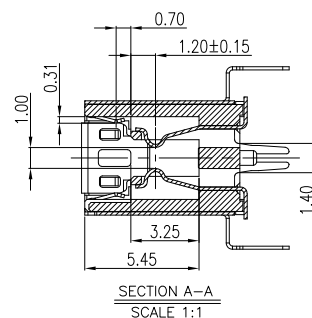
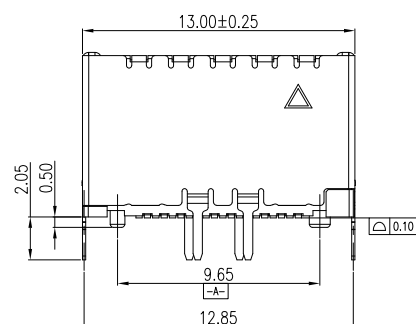
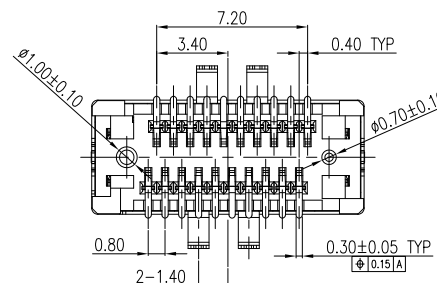
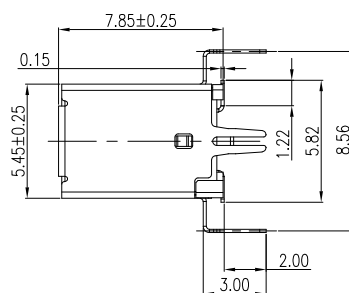
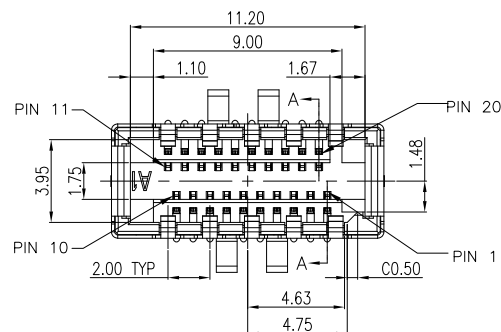
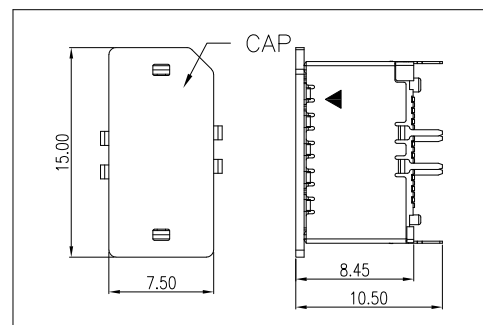
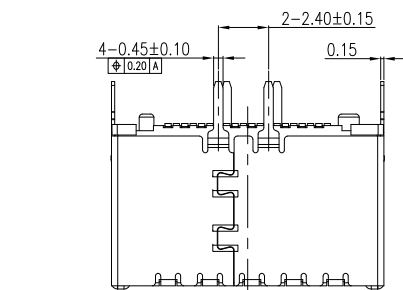
1: H=7.85mm, With Posts

A: A Key

0: Black Color

G: Gold Flash

S: SMT Type



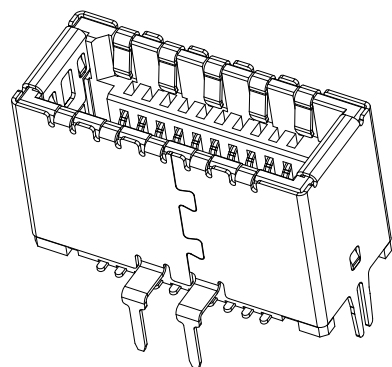
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UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN MM TOLERANCES ARE: DECIMALS ANGLES .X ± .38 ± 3° .XX ± .25 .XXX ± .20 DO NOT SCALE DRAWING			CAD GENERATED DRAWING, DO NOT MANUALLY UPDATE		凱聯科技股份有限公司 Conn-Link Technology Inc.			
			APPROVALS				DATE	
			DRAWN Chris				02/08/23	
			CHECKED Cindy				02/08/23	
			APPV Jason		02/08/23			
			SHEET 1 OF 2		SCALE NONE			
			DWG. NO.		CU3E-R20-0A1			
					REV. A			



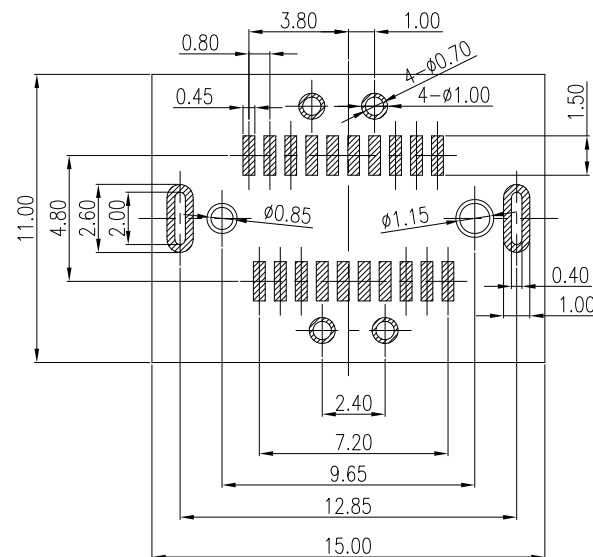
凱聯科技股份有限公司
Conn-Link Technology Inc.

DESCRIPTION USB 3.2 E Type, Receptacle, 20Pins, Vertical Type,
SMT, Gold Flash, With DIP Shell, With Posts,LCP, Black Color

1 2 3 4 5 6 7 8



3D VIEW



RECOMMEND PCB LAYOUT

Pin Map to support one Type-C port

P11	P12	P13	P14	P15	P16	P17	P18	P19	P20
VBUS	TX2+	TX2-	GND	RX2+	RX2-	GND	D-	D+	CC2
SBU2	SBU1	CC1	VBUS	RX1-	RX1+	GND	TX1-	TX1+	VBUS
P10	P09	P08	P07	P06	P05	P04	P03	P02	P01

Pin Map to support two Standard-A ports

P11	P12	P13	P14	P15	P16	P17	P18	P19	P20
VBUS			GND			GND	D-	D+	
			VBUS	RX1-	RX1+	GND	TX1-	TX1+	VBUS
P10	P09	P08	P07	P06	P05	P04	P03	P02	P01

A	02/08/23	RELEASED FOR PRODUCTION							
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		APPROVALS	DATE						
		DRAWN Chris	02/08/23						
		CHECKED Cindy	02/08/23						
DO NOT SCALE DRAWING		APPV Jason	02/08/23	SHEET 2 OF 2		SCALE NONE		DWG. NO. CU3E-R20-0A1	
								REV. A	